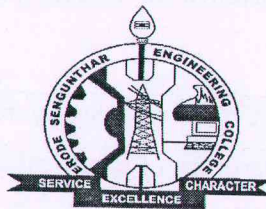


ERODE SENGUNTHAR ENGINEERING COLLEGE

(An Autonomous Institution, Affiliated to Anna University, Chennai)

PERUNDURAI, ERODE - 638 057



Department of Electrical and Electronics Engineering
5th BOARD OF STUDIES MEETING

16th July 2022 @ 02.00 p.m.

GOOGLE ONLINE MEET

<https://meet.google.com/amm-ynrj-dtk>



ERODE SENGUNTHAR ENGINEERING COLLEGE

(An Autonomous Institution –Affiliated to Anna University, Chennai)

PERUNDURAI, ERODE - 638057



DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

5th BOARD OF STUDIES MEETING (ONLINE MODE)

Meeting Link: <https://meet.google.com/amm-ynrj-dtk>

Date & Time: 16.07.2022 & 02.00 p.m.

AGENDA

The 5th Board of Studies Meeting is convened on 16.07.2022. The agenda for the meeting is as follows.

BoS /EEE 5.1	:	Welcome address and opening remarks by the Chairman, Board of Studies, Department of Electrical and Electronics Engineering.
BoS /EEE 5.2	:	Confirmation of the Minutes of 04 th Board of Studies Meeting and Action taken report of 04 th Board of Studies meeting.
BoS /EEE 5.3	:	To consider and approval curriculum & syllabus of all semesters for the B.E. Electrical and Electronics Engineering programme offered under Regulation 2019 (Both 2019 admitted and 2020 admitted).
BoS /EEE 5.4	:	To consider and approve the identified equivalent subjects to be Added /Exempted for the students readmitted from Anna University Regulations 2013/2017 to Autonomous Regulations 2019
BoS /EEE 5.5	:	To consider and approve few identified 12 weeks NPTEL online courses equivalent to credit transfer under Regulations 2019.
BoS /EEE 5.6	:	To consider and approve the syllabi of courses offered to other departments
BoS /EEE 5.7	:	Best practices followed in (external) member's institutions.
BoS /EEE 5.8	:	Suggestions for Revisions in curriculum & syllabi needed for forthcoming Regulations 2023 (R2023)
BoS /EEE 5.9	:	Any other items


CHAIRMAN

BOARD OF STUDIES - EEE



ERODE SENGUNTHAR ENGINEERING COLLEGE

(An Autonomous Institution –Affiliated to Anna University, Chennai)

PERUNDURAI, ERODE - 638057



DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

5TH BOARD OF STUDIES MEETING

(ONLINE MODE)

Meeting Link: <https://meet.google.com/yms-hfjh-rxc>

MINUTES OF THE MEETING


The 05th Board of Studies meeting of Department of Electrical and Electronics Engineering was held on 16th July 2022 at 02.00 p.m. through online (Google meet).

The following members were present

1.	Dr. P.Selvan , Professor & Head / EEE, ESEC	Chairman
2.	Dr. K. Sundareswaran Professor(HAG) Department of Electrical and Electronics Engineering National Institute of Technology, Trichirapalli, Tamil Nadu	University Nominee
3.	Dr. S. Thangavel Associate Professor, Department of Electrical and Electronics Engineering, National Institute of Technology Karaikal, Puducherry	Academic Experts
4.	Dr.P.Ponnambalam Associate Professor Department of Instrumentation Engineering, VIT, Vellore	
5.	Er.N.C.Lenin Managing Director, Shri Jai Sharath Automation & Engineering Solution, TS/77, 2 nd Floor, Thiru.Vi.Ka. Industrial Estate, Ekkattuthangal, Chennai – 600 032	Industrial Expert related to Placement
6.	Dr.T.Jayakumar Associate Professor, Department of Electrical and Electronics Engineering, Nandha Engineering College, Perundurai	Meritorious Alumnus Nominated by the Principal
7.	Dr.M.Shyamalagowri , Associate Professor / EEE	Internal Members
8.	Dr.R.Muthukumar , Associate Professor / EEE	
9.	Dr.S.Karthick , Assistant Professor / EEE	

BoS /EEE5.1	:	Welcome address and opening remarks by the Chairman, Board of Studies, Department of Electrical and Electronics Engineering.
		The Chairman, BoS welcomed and introduced the members of Board of Studies and thanked each of them for spending their valuable time to attend the meeting.
BoS /EEE5.2	:	Confirmation of the Minutes of 04th Board of Studies Meeting and Action taken report of 04th Board of Studies meeting.
		The Chairman, BoS of the Electrical and Electronics Engineering presented the Minutes of 4 th Board of Studies Meeting which was held on 30.12.2021 and action taken report of the same.
BoS / EEE 5.3	:	To consider and approval curriculum & syllabus of all semesters for the B.E. Electrical and Electronics Engineering programme offered under Regulation 2019 (Both 2019 admitted and 2020 admitted).
		The Chairman, BoS of the Electrical and Electronics Engineering presented the curriculum & syllabus of all semesters for the B.E. Electrical and Electronics Engineering programme to be offered under Regulation 2019(Both 2019 admitted and 2020). The BoS members approved the same.
BoS / EEE 5.4		To consider and approve the identified equivalent subjects to be Added /Exempted for the students readmitted from Anna University Regulations 2013/2017 to Autonomous Regulations 2019
	:	The Chairman, BoS of the Electrical and Electronics Engineering presented the identified equivalent subjects to be Added /Exempted for the students readmitted from Anna University Regulations 2013/2017 to Autonomous Regulations 2019. The BoS members approved the same.
BoS / EEE 5.5		To consider and approve few identified 12 weeks NPTEL online courses equivalent to credit transfer under Regulations 2019.
		The Chairman presented the few identified 12 weeks NPTEL online courses equivalent to credit transfer under Regulations 2019. After the interaction, BoS members approved the same.
BoS / EEE 5.6	:	To consider and approve the syllabi of courses offered to other departments
		The Chairman presented the syllabi of courses offered to other departments. The BoS members approved the same.

	<p>Best practices followed in (external) member's institutions.</p> <p>The BoS members suggested the following best practices:</p> <p>Dr.K.Sundareswaran</p> <ul style="list-style-type: none"> • In NIT, Trichy, For Theory Subjects, it is practiced to do minimum two practical experiments in Laboratory for better understanding. • In laboratory courses, first cycle comprises 5 to 6 experiments, and in second cycle students need to fabricate a model /design a mini project or they need to give solution to randomly selected problem. • Startups from second year onwards implemented. • Giving group assignment for particular problem • Offer two Credit Internship in seventh semester (International/ National/ Interdisciplinary), and validated by 3-member committee, based on performance, grade will be awarded. <p>Dr.S.Thangavel</p> <ul style="list-style-type: none"> • Assignment preparation with reference of journals & magazines (from the collection of 10 journal /Magazine) • Offering NPTEL courses related to management may be considered for the students willing to do MBA in his/her higher studies <p>Dr.P.Ponnambalam</p> <ul style="list-style-type: none"> • Project based learning/miniature model/unique assignment considered. <p>Dr.T.Jayakumar</p> <ul style="list-style-type: none"> • Project based learning considered. <p>Er.N.C.Lenin</p> <ul style="list-style-type: none"> • Internal assessment system may be strengthening to validate the internship. • Internship details may be disseminated to BoS members during meeting.
BoS / EEE 5.7	
BoS / EEE 5.8	<p>Suggestions for Revisions in curriculum & syllabi needed for forthcoming Regulations 2023 (R2023)</p> <p>BoS members advised to consider the suggested points in the best practices while revising the courses for next Regulation R2023.</p>
BoS / EEE 5.9	<p>Any other items</p> <p>The chairman informed that, the discussed suggestions are being incorporated at the maximum extent and he thanked all the members for their kind cooperation and the meeting came to an end.</p>


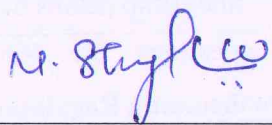
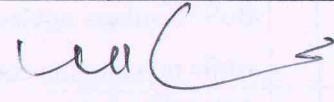


CHAIRMAN
BOARD OF STUDIES-EEE

W

MEMBERS PRESENT IN ONLINE

1	Dr. P.Selvan , Professor & Head / EEE, ESEC	Chairman
2	Dr. K. Sundareswaran Professor(HAG) Department of Electrical and Electronics Engineering National Institute of Technology, Trichirapalli, Tamil Nadu	University Nominee
3	Dr. S. Thangavel , Associate Professor, Department of Electrical and Electronics Engineering, National Institute of Technology Karaikal,Puducherry	Academic Experts
4	Dr.P.Ponnambalam Associate Professor Department of Instrumentation Engineering,VIT, Vellore	
5	Er.N.C.Lenin Managing Director, Shri Jai Sharath Automation & Engineering Solution, TS/77, 2 nd Floor,Thiru.Vi.Ka. Industrial Estate, Ekkattuthangal, Chennai – 600 032	Industrial Expert related to Placement
6	Dr.T.Jayakumar Associate Professor, Department of Electrical and Electronics Engineering, Nandha Engineering College, Perundurai	Meritorious Alumnus Nominated by the Principal

MEMBERS PRESENT

1	Dr. P.Selvan, Chairman /BOS Professor & Head / EEE, ESEC	
2	Dr.M.Shyamalagowri, Associate Professor / EEE	
3	Dr.R.Muthukumar, Associate Professor / EEE	
4	Dr.S.Karthick, Assistant Professor / EEE	



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HoD EEE is presenting

BoS /EEE 5.4	included/Exempted for the students readmitted from Regulations 2017 to Regulations 2019
BoS /EEE 5.5	To consider and approve 12 weeks NPTEL online courses for the Electrical and Electronics Engineering programme to be offered under Regulations 2019.
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BoS /EEE 5.8	Suggestions for new curriculum & syllabi for regulations 2023 (R2023)
BoS /EEE 5.9	Any other items

2:09 PM | amn-ynrj-dtk

People

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In call

- MUTHUKUMAR R (You)
- Dr. T.Jayakumar
- HoD EEE Meeting host
- HoD EEE Presentation
- karthick s
- MUTHUKUMAR R
- Ponnambalam P

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2:12 PM | amn-ynrj-dtk

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- MUTHUKUMAR R (You)
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- HoD EEE Presentation
- karthick s
- MUTHUKUMAR R
- Ponnambalam P

English (United States)
English (India)

To switch input methods, press Windows key + space

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- MUTHUKUMAR R (You)
- Dr. T.Jayakumar
- HoD EEE Meeting host
- HoD EEE Presentation
- karthick s
- MUTHUKUMAR R
- Ponnambalam P

Department of Electrical and Electronics Engineering

The 05th Board of studies meeting held on 16.07.2022 through the following link:

Google online meet link <https://meet.google.com/amm-ynrj-dtk>

The screenshot shows a Google Meet window with a presentation slide titled "19ES306 ELECTRICAL ENGINEERING LABORATORY". The slide contains the following information:

	L	T	P	C	Hours	Marks
	0	0	2	1	30	100

Course Objectives: The purpose of learning this course is:

- To give a practical knowledge on the working of electrical machines including dc machines, induction motors and synchronous motors
- To impart the basics about design and implementation of small electronic circuits

Course Outcome(s): At the end of this course, learners will be able to:

- Test and validate shunt and compound generator.
- Test and Validate series and shunt motor.
- Test and Validate transformer and induction motors
- Acquire knowledge on working of zener diodes.
- Acquire knowledge on working of rectifiers.

Exp No. Name of Experiments

1	Dhm's law and Kirzhhoff's law
2	Diode Characteristics
3	O.C.C of DC Shunt generator
4	Load characteristics of DC Shunt generator

The meeting interface shows the host "HoD EEE" and a list of participants including Prashanth Lenin, MUTHUKUMAR R, Ponnambalam P, Prashanth Lenin, Shyamalagowri M., Sundar eswaran, and Thangavel S.

The screenshot shows a Google Meet window with a presentation slide listing experiments:

7	Watt meters LPF and UPF	4
8	Resistors & Breadboards	-
9	Cathode Ray Oscilloscopes	4
10	Dual Regulated power supplies	6
11	A.C. Signal Generators	4
12	Transistors (BJT, JFET)	-

The meeting interface shows the host "HoD EEE" and a list of participants including MUTHUKUMAR R (You), Dr. T. Jayakumar, HoD EEE Meeting host, HoD EEE Presentation, karthick s, MUTHUKUMAR R, and Ponnambalam P. A notification for karthick s says "You can't turn on the mic here".